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(20140/00316)
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DECLARATION FOR PATENT APPLICATION
English Language Declaration

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Field Effect Transistor With Etched-Back Gate Dielectric

the specification of which

[] is attached hereto.

[X] was filed on December 10, 2003, as United States Patent Application Serial No. TBA or PCT International Application Number [], and was amended on [] (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with 37 CFR § 1.56(a).

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or §365(b) of any foreign application(s) for patent or inventor's certificate listed below, or § 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Priority Claimed			
(Application No.)	(Country)	(Day/Month/Year Filed)	YES NO
(Application No.)	(Country)	(Day/Month/Year Filed)	YES NO
(Application No.)	(Country)	(Day/Month/Year Filed)	YES NO

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

(Application Serial No.)	(Filing Date)
(Application Serial No.)	(Filing Date)
(Application Serial No.)	(Filing Date)

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by 35 U.S.C. § 112, first paragraph, I acknowledge the duty to disclose material information as defined in 37 CFR § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(U.S. Filing Date)	(Status) (patented, pending, abandoned)
(Application Serial No.)	(U.S. Filing Date)	(Status) (patented, pending, abandoned)
(Application Serial No.)	(U.S. Filing Date)	(Status) (patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements are made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: *(list name and registration number)*

Manny W. Schecter, Reg. No. 31,722; Kevin M. Jordan, Reg. No. 40,277, Christopher A. Hughes, Reg. No. 26,914, John E. Hoel, Reg. No. 26,279; Joseph C. Redmond, Jr., Reg. No. 18,753; Douglas W. Cameron, Reg. No. 31,596; Wan Yee Cheung, Reg. No. 42,410; Thu Ann Dang, Reg. No. 41,544; Timothy M. Farrell, Reg. No. 37,321; Louis P. Herzberg, Reg. No. 41,500; Derek S. Jennings, Reg. No. 41,473; Stephen C. Kaufman, Reg. No. 29,551; Richard M. Ludwin, Reg. No. 33,010; Daniel P. Morris, Reg. No. 32,053; Alison D. Mortinger, Reg. No. 39,306; Louis J. Percello, Reg. No. 33,206, Robert M. Trepp, Reg. No. 25,933 and Gail H. Zarick, Reg. No. 43,303, all of INTERNATIONAL BUSINESS MACHINES CORPORATION.

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Full name of fourth joint inventor (if any)

Inventor's Signature

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Date

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Full name of fifth joint inventor (if any)

Inventor's Signature

Residence Address

Date

Citizenship

Post Office Address

Same as above

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Saenger, et al. DOCKET NO: YOR920030530US1
(20140/0316)

SERIAL NO: TBA

FILING DATE: December 10, 2003

FOR: Field Effect Transistor With Etched-Back Gate Dielectric

APPOINTMENT OF ASSOCIATE ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The undersigned attorney, who has been appointed as an attorney in the Combined Declaration and Power of Attorney for the above-identified application, hereby appoints Burton A. Amernick, Reg. No. 24,852, of Customer 30678, Connolly Bove Lodge & Hutz, LLP, Suite 800, 1990 M Street, N.W., Washington, D.C. 20036-3425 telephone number (202) 331-7111, his associate attorney Matthew J. Mason, Reg. No. 44,904 to prosecute said application and to transact all business in the Patent and Trademark Office connected therewith.

In the event that the filing date and/or Application No. are not entered above at the time I execute this document, and if such information is deemed necessary, I hereby

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authorize and request Mr. Amernick to insert the filing date and/or Application No. of said application.

Respectfully submitted,

Robert M. Trepp

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